



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 7488

SERIAL NUMBER 10/706,986	FILING DATE 11/14/2003  RULE	CLASS 438	GROUP ART UNIT 2813	ATTORNEY DOCKET NO. 0756-7220
-----------------------------	---------------------------------------	--------------	------------------------	-------------------------------------

## APPLICANTS

Setsuo Nakajima, Atsugi, JAPAN;

Hisashi Ohtani, Atsugi, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\* *TN*

This application is a DIV of 09/764,432 01/19/2001 PAT 6,664,144  
which is a DIV of 09/114,223 07/13/1998 PAT 6,242,290

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *TN*

JAPAN 9-205345 07/14/1997

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 02/23/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>TN</i> Initials	JAPAN	6	51	7

## ADDRESS

31780  
ERIC ROBINSON  
PMB 955  
21010 SOUTHBANK ST.  
POTOMAC FALLS, VA  
20165

## TITLE

Method of fabricating semiconductor device

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of
------------	---	---